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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a 371 of PCT/JP00/04141 06/23/2000

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 11/181559 06/28/1999

|   |                              |                         |                       |                            |
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| Foreign Priority claimed<br><input checked="" type="checkbox"/> yes <input type="checkbox"/> no   | STATE OR<br>COUNTRY<br>JAPAN | SHEETS<br>DRAWING<br>16 | TOTAL<br>CLAIMS<br>20 | INDEPENDENT<br>CLAIMS<br>9 |
| 35 USC 119 (a-d) conditions met<br><input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance |                              |                         |                       |                            |
| Verified and Acknowledged<br>Examiner's Signature Initials  |                              |                         |                       |                            |

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## TITLE

Polysilicon semiconductor thin film substrate, method for producing the same, semiconductor device, and electronic device

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